

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3288565	memory or storage	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/27 16:58
L2	1203	1 and even and odd with cell with (row or word adj line or wordline)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/27 17:05
L3	61	2 and split adj3 gate	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/27 17:01
L4	4	3 and (split adj source or different adj source)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/27 17:06
L5	1	4 and segment	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/27 17:05
L6	118	1 and (two or "2") near3 transistor with cell with split adj3 gate	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/27 17:05
L7	1	6 and (split adj source or different adj source)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/27 17:06